

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1224	"712".clas. "processing element"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 10:26
L2	189	"712".clas. "processing element" same array near5 processor	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 10:27
L3	22	"712".clas. "processing element" same array near5 processor same "general purpose"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 10:27
L4	0	"712".clas. "array processor" with "general purpose processors"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 10:51
L5	0	"712".clas. "array processor" same "general purpose processors"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 10:51
L6	107	"712".clas. "array processor" with processors	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 10:52
L7	4	"712".clas. "array processor" with processors with "general purpose"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 10:52
L8	107	"712".clas. "array processor" with processors	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 10:53
L9	4	"712".clas. "array processor" with substrate	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 11:06
L10	2	"712".clas. multiprocessor with "common substrate"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 11:09

L11	52	"712".clas. multiprocessor with substrate	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 11:10
L12	109	"712".clas. multiprocessor with "integrated circuit"	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/11/24 11:13
S1	1	"5581767".pn.	US-PGPUB; USPAT	OR	ON	2006/03/31 12:00
S2	1361	712/10-22.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/31 12:00
S3	147	712/13.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/31 12:04
S4	33480	array with processor	US-PGPUB; USPAT	OR	ON	2006/03/31 12:04
S5	397	S4 and (halt with execution)	US-PGPUB; USPAT	OR	ON	2006/03/31 12:05
S6	397	S5 and control\$3	US-PGPUB; USPAT	OR	ON	2006/03/31 12:05
S7	114	S5 and (control\$3 near2 array)	US-PGPUB; USPAT	OR	ON	2006/03/31 12:15
S8	1	(dahl and neuss and collett).in.	US-PGPUB; USPAT	OR	ON	2006/03/31 12:16
S9	149	712/13.ccls.	US-PGPUB; USPAT	OR	ON	2006/09/29 16:49
S10	149	S9	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/09/29 16:49
S11	37394	array with processor	US-PGPUB; USPAT	OR	ON	2006/09/29 16:49
S12	418	S11 and (halt with execution)	US-PGPUB; USPAT	OR	ON	2006/09/29 16:49
S13	114	S12 and (control\$3 near2 array)	US-PGPUB; USPAT	OR	ON	2006/09/29 16:49
S14	114	S13	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/09/29 16:49
S15	1	(dahl and neuss and collett).in.	US-PGPUB; USPAT	OR	ON	2006/09/29 16:49
S16	1	S15	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/09/29 16:49

S17	41	(712/10 or 712/11 or 712/16).ccls. (stop or halt or terminate or power) near2 instruction\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/06/06 11:16
S18	19	(712/10 or 712/11 or 712/16).ccls. stop near2 instruction\$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/06/06 11:23
S19	1	(712/10 or 712/11 or 712/16).ccls. stop near5 power	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2007/06/06 11:23
S20	50086	array with processor	US-PGPUB; USPAT	OR	ON	2008/03/13 13:06
S21	482	S20 and (halt with execution)	US-PGPUB; USPAT	OR	ON	2008/03/13 13:06
S22	126	S21 and (control\$3 near2 array)	US-PGPUB; USPAT	OR	ON	2008/03/13 13:06
S23	126	S22	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/03/13 13:06
S24	126	S23	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/03/13 13:06
S25	1	(dahl and neuss and collett).in.	US-PGPUB; USPAT	OR	ON	2008/03/13 13:06
S26	1	S25	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/03/13 13:06
S27	1	S26	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2008/03/13 13:06

11/ 24/ 2008 12:30:40 PM

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